

REMARKS

Claims 1-18 are all the claims currently pending in this Application.

Claims 1-18 stand rejected under 35 U.S.C. § 103(a) as allegedly unpatentable over Norman (U.S. Patent 5,719,589) in view of Sumi (U.S. Patent 6,169,532). Applicants respectfully traverse this rejection.

With this Amendment, claim 1 is amended to clarify that the first reset voltage is non-zero and to clarify that a reset period is after a scan period for scanning each scan line. Claim 1 currently recites, *inter alia*:

during a reset period after a scan period for scanning each scan line is complete and before scanning the following scan line is started, applying a first non-zero reset voltage to all of said scan lines and applying a second reset voltage that is greater than said first reset voltage to all of said drive lines; and
scanning the following scan line immediately after the reset period in which the first reset voltage is applied to all of said scan lines and the second reset voltage is applied to all of said drive lines

The Examiner acknowledges that Norman fails to teach or suggest these limitations and therefore relies on Sumi to teach these limitations. Applicants submit that, like Norman, Sumi fails to teach or suggest these limitations.

The Examiner makes specific reference to Figure 10 and col. 13, line 63 to col. 14, line 39 of Sumi regarding these limitations. These portions of Sumi illustrate and discuss a third embodiment relating to an organic electroluminescent (EL) display. According to the third embodiment, the EL is controlled by a video signal Svd to display a still image. As shown in Figure 10, gate lines 17 are controlled by a gate driver circuit 13 and drain lines 18 are controlled by a gate driver circuit 12. Voltage can also be applied to the drain lines via reset voltage sources

41c, controlled by a reset signal 35 from the control circuit 14. It is described that after the display signals corresponding to all of the image data of one still frame are completely provided, the control circuit outputs a high level of the reset signal 35 to the reset switches 41a. Thus, a reset voltage is applied to the drain lines 18. At the same time, no voltage is applied to the gate lines 17, because the picture element signal cutting switches 22 are turned off by a drain driver off signal Sdo (col. 7, lines 55-58). Thus, after the display of a still image is completed, a reset voltage is applied to the drain lines 18, while no voltage is applied to the gate lines 17. Thereafter, scanning the first line of the next still image is performed.

Thus, the above-noted limitations of claim 1 are patentable over the teachings of Sumi because claim 1 specifically recites that a first voltage is applied to the scan lines and a second voltage is applied to the drive lines — in other words, a non-zero voltage is applied to each of the scan lines and the drive lines according to claim 1. However, according to Sumi, no voltage is applied to the gate lines, while a reset voltage is applied to the drain lines.

Additionally, according to the present invention, as recited in claim 1, the reset period is used after each scan line (i.e. B1, B2, B3, ...Bn) is complete. Therefore, claim 1 is additionally patentable over the cited references because Sumi because the Sumi reset voltage is only applied after a last scan line of a still image.

Therefore, in view of the above, Applicants submit that claim 1 is patentable over the cited references and that claims 2-18 are patentable at least by virtue of their dependence on claim 1. Applicants respectfully request that the rejection of claims 1-18 be reconsidered and withdrawn.

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned attorney at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

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